

IN THE CLAIMS:

Please cancel claims 1, 2, 3, 5, 6, 7, 8, 10, 11, 12, 13, 15, 16, 17, 18, 19 and 21 without prejudice or disclaimer.

Please amend claims 4, 9, 14 and 20 as follows:

Sub  
C1  
B2

4. (Amended) A device having at least one display panel, said display panel comprising:  
a substrate having an insulating surface;  
at least one thin film transistor formed over said substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region, and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;  
an interlayer insulating film covering said thin film transistor;  
a lead electrode comprising aluminum formed over said interlayer insulating film and electrically connected to one of the source or drain regions of said thin film transistor through a hole of said interlayer insulating film;  
an organic resin film formed over the thin film transistor, said interlayer insulating film and said lead electrode to provide a leveled upper surface; and  
a pixel electrode formed over said organic resin film, said pixel electrode being electrically connected to said thin film transistor via said lead electrode.

Sub  
C2  
B3

9. (Amended) A television comprising:  
a tuner for receiving television radio wave;  
a display panel operationally connected to said tuner, said display panel comprising:  
a substrate having an insulating surface;  
at least one thin film transistor formed over said substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region, and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

B3  
cont

an interlayer insulating film covering said thin film transistor;  
a lead electrode comprising aluminum formed over said interlayer insulating film and electrically connected to one of the source or drain regions of said thin film transistor through a hole of said interlayer insulating film;

an organic resin film formed over the thin film transistor, said interlayer insulating film and said lead electrode to provide a leveled upper surface; and  
a pixel electrode formed over said organic resin film, said pixel electrode being electrically connected to said thin film transistor via said lead electrode.

Sub  
CB

14. (Amended) A portable computer having a display panel, said display panel comprising:  
a substrate having an insulating surface;  
at least one thin film transistor formed over said substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region, and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

B4

an interlayer insulating film covering said thin film transistor;  
a lead electrode comprising aluminum formed over said interlayer insulating film and electrically connected to one of the source or drain regions of said thin film transistor through a hole of said interlayer insulating film;

an organic resin film formed over the thin film transistor, said interlayer insulating film and said lead electrode to provide a leveled upper surface; and  
a pixel electrode formed over said organic resin film, said pixel electrode being electrically connected to said thin film transistor via said lead electrode.

Sub  
CB  
B5

20. (Amended) A device having at least one display device, said display device comprising:  
a substrate having an insulating surface;  
at least one thin film transistor formed over said substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region, and a gate electrode adjacent to

said channel region with said gate insulating film interposed therebetween;

an interlayer insulating film covering said thin film transistor;

a lead electrode comprising aluminum formed over said interlayer insulating film and electrically connected to one of the source or drain regions of said thin film transistor through a hole of said interlayer insulating film;

an organic resin film formed over the thin film transistor, said interlayer insulating film and said lead electrode to provide a leveled upper surface; and

a pixel electrode formed over said organic resin film, said pixel electrode being electrically connected to said thin film transistor via said lead electrode.

Please add claims 22-53 as follows:

--22. (New) A device having at least one display panel, said display panel comprising:

a substrate having an insulating surface;

at least one thin film transistor formed over said substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region, and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

an interlayer insulating film covering said thin film transistor;

a lead electrode comprising aluminum formed over said interlayer insulating film and electrically connected to one of the source or drain regions of said thin film transistor through a first hole of said interlayer insulating film;

an organic resin film formed over the thin film transistor, said interlayer insulating film and said lead electrode to provide a leveled upper surface; and

a pixel electrode formed over said organic resin film, said pixel electrode being electrically connected to said thin film transistor via said lead electrode and through a second hole of the organic resin film,

wherein the first hole and the second hole do not overlap to each other.

23. (New) A television comprising:

a tuner for receiving television radio wave;

a display panel operationally connected to said tuner, said display panel

comprising:

a substrate having an insulating surface;

at least one thin film transistor formed over said substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region, and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

an interlayer insulating film covering said thin film transistor;

B<sub>4</sub> a lead electrode comprising aluminum formed over said interlayer insulating film and electrically connected to one of the source or drain regions of said thin film transistor through a first hole of said interlayer insulating film;

an organic resin film formed over the thin film transistor, said interlayer insulating film and said lead electrode to provide a leveled upper surface; and

a pixel electrode formed over said organic resin film, said pixel electrode being electrically connected to said thin film transistor via said lead electrode and through a second hole of the organic resin film,

wherein the first hole and the second hole do not overlap to each other.

24. (New) A portable computer having a display panel, said display panel comprising:

a substrate having an insulating surface;

at least one thin film transistor formed over said substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region, and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

an interlayer insulating film covering said thin film transistor;

a lead electrode comprising aluminum formed over said interlayer insulating film and electrically connected to one of the source or drain regions of said thin film transistor through

a first hole of said interlayer insulating film;

an organic resin film formed over the thin film transistor, said interlayer insulating film and said lead electrode to provide a leveled upper surface; and

a pixel electrode formed over said organic resin film, said pixel electrode being electrically connected to said thin film transistor via said lead electrode and through a second hole of the organic resin film,

wherein the first hole and the second hole do not overlap to each other.

25. (New) A device having at least one display device, said display device comprising:

a substrate having an insulating surface;

at least one thin film transistor formed over said substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region, and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

an interlayer insulating film covering said thin film transistor;

a lead electrode comprising aluminum formed over said interlayer insulating film and electrically connected to one of the source or drain regions of said thin film transistor through a first hole of said interlayer insulating film;

an organic resin film formed over the thin film transistor, said interlayer insulating film and said lead electrode to provide a leveled upper surface; and

a pixel electrode formed over said organic resin film, said pixel electrode being electrically connected to said thin film transistor via said lead electrode and through a second hole of the organic resin film,

wherein the first hole and the second hole do not overlap to each other.

26. (New) A device having at least one display panel, said display panel comprising:

a substrate having an insulating surface;

at least one semiconductor layer formed over said substrate and comprising at least a channel region, source and drain regions with said channel region therebetween;

Sub  
C3  
cont

a gate insulating film adjacent to said channel region;  
a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

an insulating film over at least said semiconductor layer;  
a lead electrode comprising aluminum formed over said insulating film and electrically connected to one of the source or drain regions through a first hole of said insulating film;  
an organic resin film over said insulating film and said lead electrode to provide a leveled upper surface; and  
a pixel electrode formed over said organic resin film, said pixel electrode being electrically connected to said lead electrode through a second hole of the organic resin film.

Bp

27. (New) A television comprising

a tuner for receiving television radio wave;  
a display panel operationally connected to said tuner, said display panel comprising:

a substrate having an insulating surface;  
at least one semiconductor layer formed over said substrate and comprising at least a channel region, source and drain regions with said channel region therebetween;  
a gate insulating film adjacent to said channel region; and  
a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

an insulating film over at least said semiconductor layer;  
a lead electrode comprising aluminum formed over said insulating film and electrically connected to one of the source or drain regions through a first hole of said insulating film;  
an organic resin film over said insulating film and said lead electrode to provide a leveled upper surface; and

a pixel electrode formed over said organic resin film, said pixel electrode being electrically connected to said lead electrode through a second hole of the organic resin film.

28. (New) A portable computer having a display panel, said display panel comprising:

*Sub  
comp*

- a substrate having an insulating surface;
- at least one semiconductor layer formed over said substrate and comprising at least a channel region, source and drain regions with said channel region therebetween;
- a gate insulating film adjacent to said channel region; and
- a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;
- an insulating film over at least said semiconductor layer;
- a lead electrode comprising aluminum formed over said insulating film and electrically connected to one of the source or drain regions through a first hole of said insulating film;
- an organic resin film over said insulating film and said lead electrode to provide a leveled upper surface; and
- a pixel electrode formed over said organic resin film, said pixel electrode being *By* electrically connected to said lead electrode through a second hole of the organic resin film.

29. (New) A device having at least one display device, said display device comprising:

- a substrate having an insulating surface;
- at least one semiconductor layer formed over said substrate and comprising at least a channel region, source and drain regions with said channel region therebetween;
- a gate insulating film adjacent to said channel region;
- a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;
- an insulating film over at least said semiconductor layer;
- a lead electrode comprising aluminum formed over said insulating film and electrically connected to one of the source or drain regions through a first hole of said insulating film;
- an organic resin film over said insulating film and said lead electrode to provide a leveled upper surface; and
- a pixel electrode formed over said organic resin film, said pixel electrode being electrically connected to said lead electrode through a second hole of the organic resin film.

30. (New) A device having at least one display panel, said display panel comprising:

- a substrate having an insulating surface;
- at least one semiconductor layer formed over said substrate and comprising at least a channel region, source and drain regions with said channel region therebetween;
- a gate insulating film adjacent to said channel region; and
- a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;
- an insulating film over at least said semiconductor layer;
- a lead electrode comprising aluminum formed over said insulating film and electrically connected to one of the source or drain regions through a first hole of said insulating film;
- an organic resin film over said insulating film and said lead electrode to provide a leveled upper surface; and
- a pixel electrode formed over said organic resin film, said pixel electrode being electrically connected to said lead electrode through a second hole of the organic resin film, wherein the first hole and the second hole do not overlap to each other.

31. (New) A television comprising:

- a tuner for receiving television radio wave;
- a display panel operationally connected to said tuner, said display panel comprising:
  - a substrate having an insulating surface;
  - at least one semiconductor layer formed over said substrate and comprising at least a channel region, source and drain regions with said channel region therebetween;
  - a gate insulating film adjacent to said channel region; and
  - a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;
  - an insulating film over at least said semiconductor layer;
  - a lead electrode comprising aluminum formed over said insulating film and



electrically connected to one of the source or drain regions through a first hole of said insulating film;  
an organic resin film over said insulating film and said lead electrode to provide a leveled upper surface; and  
a pixel electrode formed over said organic resin film, said pixel electrode being electrically connected to said lead electrode through a second hole of the organic resin film, wherein the first hole and the second hole do not overlap to each other.

32. (New) A portable computer having a display panel, said display panel comprising:

B6

a substrate having an insulating surface;  
at least one semiconductor layer formed over said substrate and comprising at least a channel region, source and drain regions with said channel region therebetween;  
a gate insulating film adjacent to said channel region; and  
a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;  
an insulating film over at least said semiconductor layer;  
a lead electrode comprising aluminum formed over said insulating film and electrically connected to one of the source or drain regions through a first hole of said insulating film;  
an organic resin film over said insulating film and said lead electrode to provide a leveled upper surface; and  
a pixel electrode formed over said organic resin film, said pixel electrode being electrically connected to said lead electrode through a second hole of the organic resin film, wherein the first hole and the second hole do not overlap to each other.

33. (New) A device having at least one display device, said display device comprising:

Sub C6

a substrate having an insulating surface;  
at least one semiconductor layer formed over said substrate and comprising at least a channel region, source and drain regions with said channel region therebetween;  
a gate insulating film adjacent to said channel region;  
a gate electrode adjacent to said channel region with said gate insulating film

interposed therebetween;

an insulating film over at least said semiconductor layer;

a lead electrode comprising aluminum formed over said insulating film and electrically connected to one of the source or drain regions through a first hole of said insulating film;

an organic resin film over said insulating film and said lead electrode to provide a leveled upper surface; and

a pixel electrode formed over said organic resin film, said pixel electrode being electrically connected to said lead electrode through a second hole of the organic resin film,

wherein the first hole and the second hole do not overlap to each other.

34. (New) A portable computer having a display panel, said display panel comprising:

a substrate having an insulating surface;

at least one thin film transistor formed over said substrate, said thin film transistor including at least a channel region, source and drain regions with said channel region therebetween, a gate insulating film adjacent to said channel region, and a gate electrode adjacent to said channel region with said gate insulating film interposed therebetween;

an interlayer insulating film covering said thin film transistor;

a lead electrode comprising aluminum formed over said interlayer insulating film and electrically connected to one of the source or drain regions of said thin film transistor through a hole of said interlayer insulating film;

an organic resin film formed over the thin film transistor, said interlayer insulating film and said lead electrode to provide a leveled upper surface; and

a pixel electrode formed over said organic resin film, said pixel electrode being electrically connected to said thin film transistor via said lead electrode,

wherein during applying a reference signal having a varying voltage to the other one of the source or drain regions, a select signal is applied to the gate electrode in order to perform a gradation display.

35. (New) The portable computer according to claim 34 wherein said thin film transistor is

complementarily connected to another thin film transistor and said select signal is a bipolar pulse.

36. (New) The device according to claim 4 wherein said display panel is a liquid crystal device.

37. (New) The television according to claim 9 wherein said display panel is a liquid crystal device.

38. (New) The portable computer according to claim 14 wherein said display panel is a liquid crystal device.

39. (New) The device according to claim 20 wherein said display panel is a liquid crystal device.

40. (New) The device according to claim 22 wherein said display panel is a liquid crystal device.

41. (New) The television according to claim 23 wherein said display panel is a liquid crystal device.

42. (New) The portable computer according to claim 24 wherein said display panel is a liquid crystal device.

43. (New) The device according to claim 25 wherein said display panel is a liquid crystal device.

44. (New) The device according to claim 26 wherein said display panel is a liquid crystal device.

45. (New) The television according to claim 27 wherein said display panel is a liquid crystal device.
46. (New) The portable computer according to claim 28 wherein said display panel is a liquid crystal device.
47. (New) The device according to claim 29 wherein said display panel is a liquid crystal device.
48. (New) The device according to claim 30 wherein said display panel is a liquid crystal device.
49. (New) The television according to claim 31 wherein said display panel is a liquid crystal device.
50. (New) The portable computer according to claim 32 wherein said display panel is a liquid crystal device.
51. (New) The device according to claim 33 wherein said display panel is a liquid crystal device.
52. (New) The portable computer according to claim 34 wherein said display panel is a liquid crystal device.
53. (New) The portable computer according to claim 35 wherein said display panel is a liquid crystal device.--

By  
cont.